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TOSHIHIKO OUCHI

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APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

1. ☒ Fee Transmittal Form
(Submit an original, and a duplicate for fee processing)2. ☒ Specification Total Pages **36**3. ☒ Drawing(s) (35 USC 113) Total Sheets **15**4. ☒ Oath or Declaration Total Pages **1**a. ☒ Newly executed (original or copy)b. ☐ Unexecuted for information purposesc. ☐ Copy from a prior application (37 CFR 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]i. ☐ **DELETION OF INVENTOR(S)**
Signed Statement attached deleting
inventor(s) named in the prior application, see
37 CFR 1.63(d)(2) and 1.33(b).5. ☐ Incorporation By Reference (useable if Box 4c is checked)
The entire disclosure of the prior application, from which a copy of
the oath or declaration is supplied under Box 4c, is considered as
being part of the disclosure of the accompanying application and is
hereby incorporated by reference therein.**ADDRESS TO:**Assistant Commissioner for Patents
Box Patent Application
Washington, DC 202316. ☐ Microfiche Computer Program (Appendix)7. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)a. ☐ Computer Readable Copyb. ☐ Paper Copy (identical to computer copy)c. ☐ Statement verifying identity of above copies**ACCOMPANYING APPLICATION PARTS**8. ☐ Assignment Papers (cover sheet & document(s))9. ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney
(when there is an assignee)10. ☐ English Translation Document (if applicable)11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS
Citations12. ☐ Preliminary Amendment13. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)14. ☐ Small Entity ☐ Statement filed in prior application
Statement(s) Status still proper and desired15. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)16. ☐ Other: _____

17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No. _____**18. CORRESPONDENCE ADDRESS**☒ Customer Number or Bar Code Label**05514**

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CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 CFR 1.16(c))	27-20 =	7	X \$ 18.00 =	\$ 126.00
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19. Small entity status

- a. ☐ A Small entity statement is enclosed
- b. ☐ A small entity statement was filed in the prior nonprovisional application and such status is still proper and desired.
- c. ☐ Is no longer claimed.

20. ☒ A check in the amount of \$ 894.00 to cover the filing fee is enclosed.

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22. The Commissioner is hereby authorized to credit overpayments or charge the following fees to Deposit Account No. 06-1205:

- a. ☒ Fees required under 37 CFR 1.16.
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- c. ☐ Fees required under 37 CFR 1.18.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

NAME	Shawn W. Fraser - Reg. No. 45,886
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DATE	July 27, 2000

SWF\cmv

TITLE OF THE INVENTION

OPTICAL WIRING DEVICE

BACKGROUND OF THE INVENTION

5 Field of the Invention

This invention relates to an optical wiring device, such as an optical connector and a waveguide cable, for optically connecting electronic equipment; boards in electronic equipment, or apparatuses to each other, its driving method, and so forth.

10 Related Background Art

In recent years, the speed of computers, information processing, and computer environment, including displays and printers, has been increasing. At the same time, problems of signal delay, heat generation and electromagnetic radiation emission noise (EMI) due to the electric wiring occur in connections between boards in electronic equipment, between a board and a built-in device, and between electronic equipment. Solutions of those problems are, however, difficult. It is hence apparent that limitations in the electric wiring will be a serious problem in the near future.

20 Between apparatuses, a system has been developed in which fast transmission by giga-bit Ethernet and IEEE1394 is established by using twisted pair cables or the like. As a transmission system for achieving fast transmission, a method of using a low voltage differential signal with a small amplitude (low voltage differential signalling (LVDS)) is generally used. The range of its use is, however, restricted since costs of its interface IC and cable, such as a twinax cable, are high. Further, in its high-speed transmission at about 1 Gbps, impedance matching is

required, and its transmission distance is limited to about 10 m.

Parallel connection, which requires no dedicated integrated circuit (IC), is also frequently employed between boards in an apparatus. However, when its total rate exceeds 1 Gbps, problems occur due to the
5 number of pins, connector reliability, space, skew (delay between channels), and the cost and weight of cables.

Furthermore, in either case, the problem of EMI is serious in the electric wiring, and its seriousness increases as the transmission speed becomes faster.

10 As a method for solving the problems of limitations in the electric wiring, techniques of optical connection are under course of development. In the optical connection, O (optical)/E (electric) and E (electric)/O (optical) converting portions are ordinarily provided in an apparatus, and the optical coupling is conducted by connecting an optical fiber or the
15 like using an optical connector.

Fig. 1 illustrates an example as disclosed in Japanese Patent Application Laid-Open No. 6(1994)-174981. In Fig. 1, an optical active receptacle 1001 with two-dimensionally arrayed E/O and O/E converting portions is fixed to a wiring board 1050, and an optical connector 1060
20 is inserted into the receptacle 1001 such that an optical fiber 1061 is optically coupled to the converting portions. It is thereby possible to achieve a large-capacity optical interconnection by using a bundle fiber 1063. In the structure of Fig. 1, there are further provided a driving electronic circuit 1020, an optical connecting portion 1040, an optical
25 coupling portion 1066, and a guide pin 1067.

Such a system eliminates the signal delay due to parasitic capacitance as appears in the electric wiring, signal degradation

resulting from an unstable ground, and EMI emitted from the wiring. The system is therefore expected to be the next-generation in wiring techniques. There is, however, the problem that precise implementation of optical connector and optical device is required to reduce optical losses, and hence, its cost increases and the system is difficult to put into practice. Further, when a large number of channels and optical connecting portions are needed to achieve a large-capacity transmission, problems of yield and space occur, and hence, its use in place of the electric wiring is limited. Furthermore, the system is not reliable for frequent loading and unloading, and ordinary handling is not easy. Therefore, the system is not suitable for use in electronic equipment intended for general consumers.

In the above situation, the following method has been proposed by Japanese Patent Application Laid-Open No. 9(1997)-80360. In this method, an optical coupling portion is built in and fixed to a connector, and a wiring board and an optical wiring cable are connected by the electric wiring. As illustrated in Fig. 2, an optical multi-chip-module (MCM) 1101 with integrated organic waveguide and optical modulator 1107 is fixedly coupled to an optical fiber 1100, and a modulating signal for the optical modulator 1107 is input therein through a pin 1102 for electric connection. Accordingly, when the connector with integrated pin 1102, optical MCM 1101 and optical fiber 1100 is inserted into a conventional socket 1103, the optical connection is accomplished. In the system, the connecting portion need not be precisely fabricated due to its fixed-type optical connection. Thus, cost can be reduced, loading and unloading are easy, reliability is high, and general consumers can handle the optical connector, and hence, a general-purpose connector can be achieved. In the system of Fig. 2, there are further provided an optical coupler 1105, a laser diode 1106

(continuous wave (CW) oscillation), a photodiode 1108, an amplifier 1109, and a Si substrate 1110.

In the system of Fig. 2, however, yield is likely to decrease and there is a limitation to a compact size, because a plurality of high-
5 performance devices (i.e., optical modulators 1107) are arranged on the optical MCM 1101. Further, output light from the laser diode 1106 is branched, so the light intensity is considerably decreased in the case of multi-channel. Furthermore, the module 1101 is constructed by using a plane waveguide. Accordingly, arraying is basically one-dimensional, and
10 hence, the optical cable is likely to be widened when the optical connection is conducted by using a bundle optical fiber or sheet-shaped multi-core waveguide. Thus, the size of the structure increases, and bending handling is restricted.

15 SUMMARY OF THE INVENTION

It is an object of the present invention to provide an optical wiring device which includes a built-in E/O and/or O/E converting portion, an electric connecting portion and an optical transmitting waveguide, which is readily adaptable to optical interconnection, and whose cost can
20 be reduced, and so forth.

The present invention is generally directed to an optical wiring device which includes an electric connecting portion, a optical transmission unit for transmitting an optical signal, and an optical device for conducting an optoelectric conversion. The optical device is provided
25 between the electric connecting portion and the optical transmission unit, and the optical device includes at least one of a surface light emitting device, which is modulated by an electric signal supplied through the

electric connecting portion, and a surface light receiving device, which converts an optical signal transmitted through the optical transmission means to an electric signal. In this structure, alignment and fixing between the optical device and the optical transmitting unit can be readily
5 and precisely achieved by using surface implementation, and hence, reduction of cost, enhancement of reliability and easy handling of the device can be envisaged.

The following more specific structures are possible in the above structure.

10 A plurality of the optical devices, such as one-dimensionally or two-dimensionally arrayed surface optical devices, may be arranged and integrated. The optical device may be aligned with, optically coupled to and fixed to the optical transmission unit. The electric connecting portion may be a connector which can be removably connected to an external
15 electric element.

The optical transmission unit may include a light input/output end surface, and the optical device can be surface-implemented on the light input/output end surface with the surface light emitting or receiving device abutting the light input/output end surface.

20 The surface light emitting device is typically a surface emitting laser. The surface light receiving device is typically a p-i-n photodiode, or a metal-semiconductor-metal (MSM) photodiode.

The optical wiring device may further include a wiring substrate, a plurality of the optical devices with respective independent electrodes
25 can be arranged in an array, the respective independent electrodes can be bonded to the wiring substrate in a flip-chip manner, and the electric connecting portion can include a plurality of conductors to which the

independent electrodes are electrically connected through the wiring substrate, respectively. In this case, the wiring substrate may include a window for passing light from or to the optical device, and the optical device can be optically coupled and bonded to the optical transmission unit
5 in a surface-implementation manner, with the wiring substrate interposed between the optical device and the optical transmission unit. Alternatively, the optical wiring device may further include a plate with a window for passing light from or to the optical device, and the optical device can be optically coupled and bonded to the optical transmission unit
10 in a surface-implementation manner with the plate interposed between the optical device and the optical transmission unit.

The wiring substrate is typically a flexible wiring substrate which is freely flexible. Thereby, electric implementation of the optical device can be readily achieved in the optical wiring device. Where a
15 flexible substrate, such as a tape automated bonding (TAB) tape, is used, even a three-dimensional electric wiring is possible and the degree of the electric implementation can be enlarged.

The optical wiring device may further include an electronic circuit device for driving the optical device, which is integrated on the electric
20 connecting portion. The electronic circuit device may have a parallel-to-serial converting function. When such an IC for driving the optical device or performing the parallel-to-serial conversion of a signal is thus integrated, the space required for a printed circuit board in external electronic equipment can be reduced.

25 The optical wiring device may further include an electronic circuit device in which an integrated circuit (IC) and the optical device electrically connected to the IC are integrated on a common Si substrate

in a hybrid manner and a bare chip of the optical device is implemented on the common Si substrate in a flip-chip manner. Thereby, the optical wiring device can be made compact.

5 The optical device may be a surface emitting laser sandwiched between the optical transmission unit and the wiring substrate, and the surface emitting laser may be comprised of a cavity with an active layer sandwiched between multi-layer reflective mirrors with its growth semiconductor substrate removed. Alternatively, the optical device may be a surface emitting laser sandwiched between the optical transmission
10 means and a plate, and the surface emitting laser may be comprised of a cavity with an active layer sandwiched between multi-layer reflective mirrors with its growth semiconductor substrate removed. In this structure, since the growth substrate for forming the optical device thereon, such as a GaAs or InP compound semiconductor substrate, is removed,
15 a very compact construction capable of high-speed driving can be obtained with a high degree of environmental safety.

The optical transmission unit may be a waveguide sheet in which waveguide cores are arranged in an array and which can be fabricated by photolithography and etching. Arrayed optical devices can be readily
20 abutted the waveguide cores, respectively, and a transmission capacity can be increased by using a spatial multiplexing optical transmission.

The optical transmission unit may also be an array of optical fibers, to whose end surfaces the arrayed optical devices can be optically coupled in a perpendicular light input/output manner and fixed.

25 The optical transmission unit may include a metal thin film for conducting an electric connection. The optical transmission unit can perform the electric connection as well as the optical transmission. When

an electric wiring pattern of the metal thin film is bonded to the optical transmission unit, the electric connection for a low-frequency signal, an electric power source, or a ground can be carried out as well.

5 The electric connecting portion may be constructed such that the electric connecting portion is removably connected to an external receptacle formed in an electronic apparatus. Thereby, optical interconnection can be achieved from a wiring board in the electronic apparatus through the optical wiring device of the present invention.

10 The electric connecting portion may also be constructed such that the electric connecting portion is electrically connected with a solder to a printed board provided in an electronic apparatus. Thereby, a space on the printed board can be reduced.

15 The optical device may be a surface emitting laser, which is driven directly by the on-and-off of a complementary metal-oxide semiconductor (CMOS) buffer at an output stage of an external apparatus that is connected to the electric connecting portion, and a driving current of the surface emitting laser may be controlled by a resistor inserted serially with the surface emitting laser.

20 The optical device may also be a surface emitting laser, which is driven directly by the on-and-off of a CMOS buffer at an output stage of an electronic circuit which is provided between the surface emitting laser and the electric connecting portion and electrically connected to the electric connecting portion, and a driving current of the surface emitting laser may be controlled by a resistor inserted serially with the surface emitting laser. Here, the on-off driving of the surface emitting laser is performed by switching the laser by a transistor at the output stage of a large scale integrated circuit (LSI) or the like, the resistor and

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the laser are serially connected to a voltage source, and an amount of current injected into the laser is determined by its resistance value.

Further, the present invention is directed to an electronic apparatus, in which a printed circuit board with an electronic integrated device, such as a large scale central arithmetic processing unit (a microprocessor unit (MPU)) and a random access memory (RAM), mounted thereon and an apparatus, such as an MCM and a memory, are connected to each other by the above optical wiring device. In this structure, high-speed signal processing is possible, space for the cable can be reduced, the problem of EMI can be moderated, and no design for high-frequency matching is required.

Furthermore, the present invention is directed to an electronic apparatus, wherein a computer body with a device, such as a central processing unit (CPU), mounted therein and a display with an input/output (I/O) port are connected to each other by the above optical wiring device, and/or wherein the computer body and a network, such as a local area network (LAN), are connected to each other by the above optical wiring device. Also in this structure, the same technical advantages as above can be obtained.

These advantages and others will be more readily understood in connection with the following detailed description of the more preferred embodiments in conjunction with the drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a view illustrating a prior art optical connector with its half cross-sectional.

Fig. 2 is a perspective view illustrating a prior art optical wiring

connector with its part cut-away.

Fig. 3A is a perspective view illustrating a disassembled structure of an optical wiring connector.

Fig. 3B is a perspective view illustrating a disassembled structure of an E/O converting portion in a first embodiment according to the present invention.

Fig. 4 is a perspective view illustrating the disassembled structure of integrated O/E and E/O converting portions in an optical wiring connector according to the present invention.

Fig. 5 is a perspective view illustrating the disassembled structure of an L-shaped optical wiring connector according to the present invention.

Figs. 6A to 6F are cross-sectional views illustrating a method of fabricating a waveguide sheet with arrayed waveguides in the first embodiment.

Fig. 7 is a perspective view illustrating a sheet with integrated optical wiring and electric wiring.

Figs. 8A to 8D are cross-sectional views illustrating a method of fabricating a surface emitting laser with only its functional layer left.

Figs. 9A to 9C are cross-sectional views illustrating another fabrication method of a surface emitting laser with only its functional layer left.

Figs. 10A and 10B are schematic diagrams illustrating methods of driving a light emitting device in an optical wiring device of the present invention.

Fig. 11A is a perspective view illustrating the disassembled structure of an optical wiring connector of a second embodiment according

to the present invention.

Fig. 11B is a cross-sectional view illustrating a method of connecting the second embodiment to a board.

Fig. 11C is a cross-sectional view illustrating another method of connecting the second embodiment to a board.

Fig. 12 is a cross-sectional view illustrating an optical wiring device in which a surface emitting laser and an LSI are provided on a common substrate.

Fig. 13 is a perspective view illustrating an example of inter-board connection using an optical wiring device of the present invention.

Fig. 14 is a perspective view illustrating the disassembled structure of an optical wiring connector of a third embodiment according to the present invention.

Fig. 15 is a perspective view illustrating an example of computer equipment using an optical wiring device of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

(First Embodiment)

Fig. 3A illustrates structures of electric and optical connecting portions of a first embodiment of the present invention directed to an optical wiring device. In the optical wiring device of Fig. 3A, a connecting portion 7 for electric connection is provided, and the electric connecting portion 7 includes a recessed electric coupler of two-dimensionally arranged recesses 8 and two-dimensionally arranged pins 9 for electrically connecting the corresponding recesses 8 to rear-stage electric wires. The connecting portion 7 is composed of a conventional

resin, for example. There are sixteen ($16=2\times 8$) pins 9 in the structure of Fig. 3A, but the number thereof is not limited to this configuration. The recesses 8 may be replaced by protruding pins.

At the rear of the connecting portion 7, there is provided a plate 4 including two-dimensionally arranged holes 5, serially arranged electrode pads 6, and a wiring pattern (not shown) for electrically connecting each hole 5 to each pad 6. The pins 9 of the connecting portion 7 are inserted into and soldered to the holes 5 of the plate 4, respectively, and the plate 4 is thus fixed to the connecting portion 7. The plate 4 may also be formed of a conventional resin that is usually used to form an electric printed board. When necessary, an electronic circuit for driving an optical device (later described in detail) may be formed on the plate 4.

Further, at the rear of the plate 4, there is provided an E/O and/or O/E converting portion 1 bonded to the plate 4. Surface optical devices of the converting portion 1 are integrated with a multi-core waveguide 3. Each optical device is electrically connected to each electrode pad 6 by a flexible substrate 18, such as a TAB tape, which includes an electric wiring pattern formed thereon. After all the components are assembled, the entire structure is covered with a protector, or molded with resin for easy handling.

Fig. 3B also illustrates a detailed structure of the converting portion 1. In the case of the E/O converting portion, a laser array 11 of two-dimensionally arranged surface emitting lasers is so implemented as to be sandwiched between two plates 10 and 19. An independent wiring 16 to each optical device is formed on the TAB tape 18. More specifically, each ring electrode 13 formed around a light radiating portion 12 of each

laser is aligned with each ring electrode 17 on the TAB tape 18, and these ring electrodes 13 and 17 are connected in a flip-chip manner. In the flexible substrate 18, a window 14 for passing light therethrough is formed in the ring electrode 17 corresponding to each light radiating portion 12.

5 A common electrode of the surface emitting lasers may be provided on the side of the plate 10, or the TAB tape 18. Each electrode 17 is connected to each electrode pad 15 through the wiring 16. The electrode pad 15 is connected to the electrode pad 6 formed on the plate 4.

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10 The plate 19 is fixed to the multi-core waveguide 3 by an adhesive agent such that each surface emitting laser is aligned with each core 21 of the waveguide 3. The light radiation angle of the surface emitting laser is small, say less than 10° . Therefore, when the size of the core 21 is about a square of $100\ \mu\text{m} \times 100\ \mu\text{m}$, optical loss is small even if the thickness of the plate 19 is about $100\ \mu\text{m}$. Further, tolerance of
15 optical implementation is about 20 to $30\ \mu\text{m}$, so such a passive alignment, in which a guide pin 22 formed on the plate 10 is inserted into a hole 23 formed in a supporting plate 20 of the multi-core waveguide 3, will suffice. Where a bundle of optical fibers with a core diameter of $50\ \mu\text{m}$ is used as the waveguide, it is preferable to fit a lens into the window 14 for
20 the purposes of increasing a coupling efficiency.

It is preferable to form the plates 10 and 19 and the TAB tape 18 of material with a good thermal conductivity for effective heat radiation of the laser array 11. The plates 10 and 19 are formed of metal, or a ceramic thin film of Al_2O_3 or AlN , and the TAB tape 18 is formed of a
25 polyimide film containing Al_2O_3 powders. Further, a metal film and a heat-radiating means, such as a fin (not shown), can be bonded to a surface of the plate 10, on which the plate 4 is bonded, to increase the heat

radiation efficiency from the laser array 11.

The description has been made to the E/O converting device in the foregoing. Instead, in the case of the O/E converting device, a p-i-n photodiode (PD), a metal-semiconductor-metal (MSM) device or the like is employed as a surface light receiving device, and this device is implemented in the same manner as that of the surface emitting laser. Electrodes of the p-i-n photodiode are formed in approximately the same manner as that of the above electrodes of the surface emitting laser, and electrodes of the MSM device are formed as comb-shaped electrodes on a common surface.

In the above-discussed optical wiring connector, one-way connection is assumed, in which one end of the waveguide (optical cable) 3 is coupled to the E/O converting portion and the other end thereof to the O/E converting portion. It is naturally possible to achieve a bi-directional connection in which each end includes both the E/O and O/E converting portions. In this case, there are provided both a laser array 11 of the surface emitting lasers and an array 24 of surface light receiving devices with a light receiving portion 25 and an electrode 26 implemented in a flip-chip manner on a common plate 19, as illustrated in Fig. 4. The remaining structure of Fig. 4 is the same as that of the above-discussed structure. It is naturally possible to fabricate the surface emitting lasers and surface light receiving devices on the same substrate as illustrated in Fig. 3B.

Further, while the insertion direction of the connecting portion is the same as the extension direction of the optical cable 3 in the above structure, these directions may differ from each other as illustrated in Fig. 5. In Fig. 5, the insertion direction of the converting portion forms

right angles relative to the extension direction of the optical cable 3 to construct an L-shaped connector.

The multi-core waveguide 3 for optical wiring will be described. The waveguide 3 can be preferably and conveniently formed of fluorine-
5 contained PMMA, epoxy resin, polyimide, or the like. A fabrication method of the waveguide 3 will be described by reference to Figs. 6A to 6F.

As illustrated in Fig. 6A, an optical resin 42 for forming a clad is initially coated on a Si-substrate 41 using a spinner or the like, and then hardened. Thereafter, a layer 43 with a slightly-high refractive
10 index for forming a core is coated in the same manner, and a photoresist 44 is then deposited and shaped into a core pattern by photolithography and etching.

As illustrated in Fig. 6B, after a core pattern is formed by a reactive ion etching (RIE) using oxygen plasma, the photoresist 44 is
15 removed. This core pattern is then buried with a cladding resin 42, and its surface is flattened by an etch-back process, as illustrated in Fig. 6C.

As illustrated in Fig. 6D, another array of cores 43 are then similarly formed. Here, positions of the lower and upper cores 43 can be
20 precisely aligned with each other by using alignment marks of a mask. Further, as illustrated Fig. 6E, a cladding layer of resin 42 is coated on the upper cores 43, and flattened similarly.

Finally, as illustrated in Fig. 6F, a metal thick layer 20 is formed on the uppermost cladding layer 42, and the Si-substrate 41 is removed by
25 mechanical polishing and wet etching using KOH. Thus, the flexible multi-core waveguide or cable 3, as illustrated in Figs. 3A and 3B, is obtained.

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The core 43 has a cross section of $100\ \mu\text{m} \times 80\ \mu\text{m}$, and the upper and lower cores 43 are arranged at intervals of $250\ \mu\text{m}$, respectively. The metal layer 20 acting as a supporting substrate is formed of copper (Cu) with a thickness of $100\ \mu\text{m}$. The metal layer 20 may be formed by plating, or by a method of bonding a copper thin film. The material of the metal layer 20 is not limited to copper. When the metal layer 20 is thus formed, the layer 20 functions as a heat radiator of the optical device as well as a supporter for the waveguide 3. Furthermore, when used in an equipment, the metal layer 20 can also be used to connect electric common lines between boards. When the metal layer is formed as a wiring pattern 27 as illustrated in Fig. 7, the metal layer 27 can also conduct an electric connection. The wiring pattern can be utilized as a connecting line of relatively-slow signals or analog signals, or as a power-supply line. In this case, the number of the pins 9 in the connecting portion 7 is increased, and some of the pins 9 are connected to such electric wiring.

The waveguide can be formed of various materials, and silica glass (phospho-silicate glass (PSG)) is preferable as a low refractive-index material. Where the PSG doped with P is used, the multi-layer optical wiring layer can be readily fabricated since the surface of the layer is flattened due to mass transport when heated. In this case, when the refractive index of the core layer 21 is controlled by using GPSG which is further doped with Ge, the waveguide can be readily fabricated.

The surface emitting laser for performing the E/O conversion will be described. The surface emitting laser ordinarily has a construction in which a structure sandwiching a cavity with an active layer between distributed Bragg reflector (DBR) mirrors is epitaxially grown on an n-type substrate and a current constricting structure is provided to attain a

current flow only into its light radiating portion. Such lasers can be readily arranged in a two-dimensional array as illustrated in Fig. 3B. In this embodiment, an AlAs/AlGaAs multi-layer epitaxial mirror is grown on a GaAs substrate, and a surface emitting laser of a 830-nm band with a
5 GaAs/AlGaAs multiple quantum well active layer is fabricated.

In the above structure, the common electrode is a cathode, and the electrode 13 for independent driving is an anode. Fig. 10B illustrates an E/O converting portion for driving a surface emitting laser 82. A portion at the last-stage of an LSI connected to the pin 9 of the connecting
10 portion 7 normally includes a CMOS buffer inverter 81 to carry out a current driving through the pin 9. In this embodiment, the last-stage construction is unchanged, and the E/O conversion is achieved by the surface emitting laser 82 to couple the converted light to the waveguide 3, in place of connection by a metal cable. The driving-current capability
15 of the CMOS buffer is normally less than 10 mA, and this capability sufficiently achieves the driving since the threshold current and operating current of the above laser 82 at the time of output of 100 μ W are very low, say about 1 mA and 3 mA, respectively. When a current injected into the surface emitting laser 82 is 3 mA, its operating voltage
20 is approximately 2.5 V. Accordingly, in the case of the CMOS 81 of 3.3 V, a serial resistor R to be inserted only needs to have a resistance of 267 Ω ($= (3.3 - 2.5) / 3 \times 10^{-3}$). The resistor R can be inserted into the wiring (not shown) on the plate 4 in Fig. 3A. In this system, however, there is a limitation on high-speed operation because the switching time
25 of a p-channel of the CMOS 81 is dominant due to its cathode-common operation.

In contrast, in the case of an anode-common type as illustrated

in Fig. 10A, a high-speed operation can be further promoted because the switching time of an n-channel of the CMOS 81 is dominant. Therefore, in this embodiment, it is possible to remove the n-substrate of the surface emitting laser and separate the n-side electrodes from each other to achieve the anode-common system. Figs. 8A to 8D illustrate its fabrication method.

For simplicity, only two arrayed surface emitting lasers are illustrated in Figs. 8A to 8D. As illustrated in Fig. 8A, after an n-AlAs layer (not shown) of an etch stop layer and an n-GaAs layer (not shown) of a contact layer are grown on an n-GaAs layer 60, then an n-AlAs/AlGaAs multi-layer mirror 61, a one-wavelength cavity 62 consisting of an undoped GaAs/AlGaAs multiple quantum well active layer and an AlGaAs layer, and a p-AlAs/AlGaAs multi-layer mirror 63 are epitaxially grown using a metal organic-chemical vapor deposition (MO-CVD) or the like. An annular recess 67 is then formed by etching to form a current constricting layer 66. An insulating layer 64 of SiN_x is laid down over the wafer, except over the light radiating region, and a common p-side electrode 65 is formed.

As illustrated in Fig. 8B, after the p-side electrode 65 is entirely bonded to an overall electrode (not shown) of the plate 10 with an AuSn solder, the GaAs substrate 60 is removed by polishing and chemical etching. Here, a mixture solution of H_2O_2 and NH_3 is used as an etchant, and hence, the etching can be stopped at the AlAs layer grown on the GaAs substrate 60. Immediately thereafter, the AlAs layer is removed by HCl to expose the GaAs layer grown as a lowermost layer of the mirror 61.

As illustrated in Fig. 8C, inter-device portions of the exposed mirror layer 61 are then wet-etched by an etchant of sulfuric-acid series to form a separating groove 68, and the n-side electrode 13 with the window

12 is formed for each optical device.

As illustrated in Fig. 8D, the TAB tape 18 with the electrodes 17 around the holes 14 is then bonded to the plate 19, and the electrode 17 and the electrode 13 of the surface emitting laser are bonded to each other with an AuSn solder. There can be thus provided an array of anode-common surface emitting lasers whose light can be taken through the window 14. The plate 19 is bonded to an end surface of the multi-core waveguide 3 in such a manner that the laser is aligned with and coupled to the core 21 of the waveguide 3.

A slightly different construction can also be fabricated by a fabrication method as illustrated in Figs. 9A to 9C. In this structure, the TAB tape 18 for wiring is attached to the plate 10 on an opposite side to the waveguide 3, a little different from the structure of Fig. 8D.

In Fig. 9A, the surface emitting laser is fabricated similarly to the case of Fig. 8A, but a window 70 for passing light is opened in the p-side electrode 65 to take out light through the p-side mirror layer 63. Further, the p-side is bonded to a quartz glass plate 69 with electron wax or the like.

As illustrated in Fig. 9B, the GaAs substrate 60 is then removed and an electrode 71 is formed for each device, similarly to the case of Fig. 8C. Here, a through-hole electrode 74 and an insulating layer 75 are formed on a region without the light radiating portion and a pad 76 of the anode electrode is formed on the same side as the cathode electrode, such that the p-side electrode 65 of a common electrode can be taken out from the same side as the n-side electrode. The plate 10 is then bonded to the TAB tape 18, and electrodes 72 and 73 on the TAB tape 18 are connected to the cathode electrode 71 and the common anode electrode 76 of the laser,

respectively. There is no need to open a window in the electrodes 71 and 72 since light is not taken out from this side. The p-side is bonded to the above quartz glass plate 69 to facilitate those works.

As illustrated in Fig. 9C, the glass plate 69 is then removed, and the electrode 65 on this side is aligned with and optically coupled to the core 21 of the waveguide 3. Thereafter, the electrode 65 is bonded to the end surface of the waveguide 3. In this structure, optical loss is very little since the light emitting portion is quite close to the core 21.

In such a structure without the GaAs substrate 60, the functional layer for oscillating laser light is thin, the E/O converting portion is very compact, and the inclusion ratio of As in the construction can be largely reduced, leading to enhancement of the environmental safety. Further, the functional portion is sandwiched between the two plates 10 and 19 as illustrated in Fig. 8A to 8D. Accordingly, when those plates 10 and 19 are formed of material having a large thermal conductivity, characteristics of the laser can be further enhanced.

A light emitting diode (LED) can also be used as the surface light emitting device, but in this case the operating current goes up by one order to about 30 mA, compared with the surface emitting laser. The required electric power is thus increased, and hence, its driver portion needs to be modified.

In the above description, the surface light receiving device is not described in detail, but its construction and fabrication method are similar to those of the surface light emitting device. A p-i-n structure of GaAs is fabricated by using epitaxial growth and diffusion process, for example. Si or InGaAs may also be used. In the case of the MSM type, comb-shaped electrodes of Al or the like are formed on GaAs. In a driving

circuit for the light receiving device, an amplifier and a discriminating circuit are needed. Those electronic circuits may be formed on the plate 4 with the wiring formed thereon at the rear stage of the connecting portion 7.

5 In the foregoing, the example of 830-nm band is described, but an InGaAs structure of 0.98- μ m band, an InGaAsP structure of 1.3- μ m band or the like can also be employed.

(Second Embodiment)

10 The first embodiment is directed to an optical wiring device which is principally used for parallel wiring. In this case, as the number of pins increases, the area occupied by the connecting portion and the volume of the multi-core waveguide also increase.

15 In the light of such a disadvantage, a second embodiment is directed to an optical wiring device in which parallel-to-serial conversion is partially conducted to decrease the number of pins and the features of the surface optical device (i.e., high-speed transmission) are utilized. In the second embodiment, a connecting pin 91 is fixed to a base plate 90 as illustrated in Fig. 11A. The protruding pin 91 is connected to a board 100 through a recessed receptacle 98 mounted to the board 100 as illustrated
20 in Fig. 11B, or directly bonded to the board 100 with a solder 103. At the rear of the base plate 90, there is provided a bare chip 92 of a Si-IC in which a parallel-to-serial conversion IC and a laser-driving IC are integrated. A surface optical device 93 is stacked on the Si-IC 92 in a flip-chip implementation manner as indicated by a phantom line in Fig. 11A.
25 The optical device 93 is bonded to a waveguide 95 in such a manner that each optical device 93 is aligned with and optically coupled to each core 94 of the waveguide 95. An end socket 96 is bonded to the waveguide 95

to achieve functions of protection and easy handling.

Structures of the Si-IC 92 and the optical device 93 will be described. Fig. 12 illustrates a cross section of the optical device (surface emitting laser) 93. The Si-IC 92 illustrated in Fig. 12 is implemented on the base plate 90 (not shown in Fig. 12), and electrically connected to the pins 91. The Si-IC 92 has a region in which the optical device 93 is implemented, and the optical device 93 is connected to the electrode of the Si-IC 92 through a wiring 106. The wiring 106 and the surface optical device 93 are implemented in a flip-chip manner with the electrodes 71 and 76 being aligned with the wiring 106 as illustrated in Fig. 12.

The cross section of the surface optical device 93 in Fig. 12 is the same as that of the surface emitting laser in the first embodiment of Figs. 9A to 9C. In Fig. 12, the same functional portions as those in Figs. 9A to 9C are designated by the same reference numerals, with the exception that the TAB tape 18 is omitted and a ceramic plate 105 is provided to enhance a thermal conductivity of the surface bonded to the waveguide 95 and reinforce the optical device 93 in Fig. 12. Here, the element 92 is described as the Si-IC, but this is a simple wiring substrate when no serial-to-parallel conversion is required.

Further, the surface emitting laser is described in the foregoing, but the surface light receiving device can be similarly implemented. Where four ($4=2 \times 2$) cores 94 are arranged as illustrated in Fig. 11A, two surface emitting lasers and two surface light receiving devices are arranged, for example.

The method of connection to the board 100 will be described in detail referring to Figs. 11B and 11C. Fig. 11B illustrates the case where

the socket 98 is provided on the board 100, and the pin 91 is connected to a leaf spring 99. A pin 101 extending from the socket 98 is bonded to the board 100 with a solder 102. Fig. 11C illustrates the case where no socket is provided on the board 100, and the pin 91 is bonded directly to the board 100 with the solder 103. In this case, the pin 91 can be changed to a flat type which extends in parallel with the base plate 90 such that the pin 91 can be surface-implemented.

In Figs. 11B and 11C, a member 97 for reinforcement and covering is provided, and the member 97 is implemented after the base plate 90, the Si-IC 92, the optical device 93 and the waveguide 95 are stacked. The member 97 is not illustrated in Fig. 11A. The multi-core waveguide 95 with the cores 94 may be similar to that of the first embodiment, but a waveguide with one-dimensionally arrayed cores may be used where the number of cores is small. A fiber array with one-dimensionally arranged optical fibers is also usable.

The entire structure constructed by using the thus-fabricated optical wiring device of the first or second embodiment is illustrated in Fig. 13. In Fig. 13, reference numeral 30 indicates a mother board constructed as an optoelectric mixture substrate. Reference numeral 32 indicates a large scale central arithmetic processing unit (a microprocessor unit (MPU)). Reference numeral 31 indicates a primary cache memory. Reference numeral 33 indicates a structure in which an MCM with DRAMs mounted thereon is mounted to the mother board 30 as a daughter board. The signal transmission in the boards 30 and 33 is performed through a wiring 34, but in the case of fast signals a part thereof can be transmitted through the waveguide. Parallel connection between the boards is conducted through a connecting portion 35 with a built-in

optoelectric converter and a multi-core waveguide 36 of the first embodiment. The volume of the waveguide 36 is smaller than that of a conventional electric wiring, and the former is also more flexible than the latter. Therefore, even when the density of the wiring is high,
5 loading and unloading of the waveguide 36 is easy. Further, no interference of electromagnetic waves exists at the wiring portion, so the problems of cross-talk and radiation noise are solved. At the same time, the apparatus can be readily designed since there is no need for impedance matching at the connecting portion 35.

10 With the signal exchange with an exterior memory unit such as a hard disc drive, it is preferable to conduct a serial rapid transfer by using a connector 38 and a multi-core waveguide 37 of the second embodiment at a relatively low cost. Therefore, a transmitting/receiving unit is provided in the connector 38 to perform a parallel-serial conversion and
15 achieve a transfer rate of about 10 Gbps. Although only main portions of the boards are illustrated in Fig. 13, a next-generation computer with a clock rate of over 1 GHz can be constructed by forming a necessary circuit structure and connecting the boards or electronic equipment built in the apparatus through the optical wiring of the present invention.

20 In the above structure, as compared with the case using the LVDS system, faster and smaller structures are possible, the required electric power can be reduced, and the problem of EMI can be readily solved. Also with its improved reliability, and ease of loading and unloading of the cable, and lower cost, the optical wiring of the present invention is more
25 advantageous than an optical interconnection using a conventional optical connector. Further, since even faster and smaller constructions are required and a further decrease in EMI is indispensable in recent

electronic equipment, such as computers, portable phones and digital cameras, the optical wiring of the present invention is quite effective for those equipment.

(Third Embodiment)

5 In the first and second embodiments, the multi-core waveguide fabricated by the surface process is used as the optical cable. However, when the length of the wiring between apparatuses, for example, is over 1 m, the structure using an optical fiber is more advantageous in a total cost. Therefore, in a third embodiment, an optical wiring connector is
10 constructed by using a fiber array.

Fig. 14 illustrates the structure of the third embodiment. As illustrated in Fig. 14, pins 121 for electric connection are arranged on an electric connecting portion 120, and a Si-IC 122 is implemented on the connecting portion 120 in a flip-chip manner such that the Si-IC 122 is
15 connected to the pins 121.

In the Si-IC 122, similarly to the second embodiment, driving ICs for the surface emitting laser and the surface light receiving device are integrated. The Si-IC 122 has a region for the implementation of optical devices 123 and electrode pads, similarly to the second embodiment. The
20 optical device 123 is stacked on the Si-IC 122. The optical devices 123 include the surface emitting laser and the surface p-i-n PD. Those optical devices 123 may be formed on a common substrate, or they may be separately fabricated and implemented on a common Si-IC in a hybrid form and in a flip-chip manner. The Si-IC 122 and the optical device 123 can be stacked
25 similarly to the second embodiment.

As a waveguide 130, an array of four fibers with a clad 127 and a core 128 arranged at a pitch of 250 μm is used. Here, a GI(graded

index)-50 is used as the fiber, but a single mode fiber or a plastic optical fiber (POF) may also be used. Among the POFs, an HPCF fiber with a quartz core 128 and a plastic clad 127 is preferable. The diameter of the quartz core 128 is about $200\text{ }\mu\text{m}$, and hence, its optical coupling is easy and optical loss is low. Further, its cost can be reduced.

The fiber array can be readily fabricated with good precision, as follows. As illustrated Fig. 14, fibers are arranged on V-shaped grooves of a Si substrate 126, and the fibers are covered by a flat Si substrate 125 and fixed with an adhesive. A member 129 for reinforcement and handling is bonded to an end portion of the waveguide (fiber array) 130, and its end surface is polished. A light receiving/emitting portion 124 of the optical device 123 stacked on the above Si-IC 122 is aligned with and optically coupled to the optical fiber, and the end surface of the reinforcing member 129 is then bonded to the optical device 123. Finally, a cover member (not shown) for reinforcement is further bonded between the connecting portion 120 and the member 129, or the entire structure is molded with a molding material. The optical wiring device or connector with a optoelectric conversion function is thus completed.

In the device, the optical fiber is used, so the distance of transmission can be freely selected in a range up to over 100 m. Further, there is no problem of electromagnetic noise because of the optical transmission, and there is no need of impedance matching at the connecting portion. Moreover, since the connecting portion has no optical coupling portion, reliability is high and no danger exists at the time of loading and unloading.

Such an optical wiring device can be employed for fast transmission between electronic equipment using a next-generation IEEE1394, a Gbit-

class Ethernet between computers, and the like. An example is illustrated in Fig. 15.

In Fig. 15, a body 133 of a personal computer (PC) is connected to a liquid-crystal (LC) display 134 through an optical wiring device 132 of the present invention. On a front surface of the LC display 134, there are provided a port 141, such as a universal serial bus (USB), to which a keyboard 137 or an external memory 140 can be connected, and an IEEE1394 port 142 to which a digital video apparatus 139 is connected. A mouse 138 is spatially connected to a sensor 143 provided on the front of the LC display 134, through infrared rays. Such a construction is an example of connection in a PC in course of development for next generation. When connecting ports are formed on the display front user interface, fast transmission is required between the PC body 133 and the display 134. Here, when the optical wiring device 132 with the electric connecting portion of the present invention is used as the wiring, a connection capable of fast transfer can be attained at low cost and with low EMI. In this construction, no optical coupler is used in the connecting portion, and hence, general consumers can readily handle the optical wiring device.

Also, with connections to a hub 136 and a local area network (LAN), GIII-class connections are being required. When an optical wiring cable 135 of the present invention is used, reductions of cost, weight and space of the cable can be reliably achieved. In the above embodiments, the connecting portion only includes pins for electric connections, but a part of the connecting portion may include an optical connector for optical coupling.

While the present invention has been described with respect to what is presently considered to be the preferred embodiments, it is to be

understood that the invention is not limited to the disclosed embodiments.

The present invention is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims.

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WHAT IS CLAIMED IS:

1. An optical wiring device comprising:
an electric connecting portion;
optical transmission means for transmitting an optical signal; and
5 an optical device for conducting an optoelectric conversion, said
optical device being provided between said electric connecting portion and
said optical transmission means, and said optical device including at least
one of a surface light emitting device, which is modulated by an electric
signal supplied through said electric connecting portion, and a surface
10 light receiving device, which converts an optical signal transmitted
through said optical transmission means to an electric signal.

2. An optical wiring device according to claim 1, wherein a
plurality of said optical devices are arranged and integrated.

3. An optical wiring device according to claim 1, wherein said
optical device is aligned with, optically coupled to and fixed to said
optical transmission means.

4. An optical wiring device according to claim 1, wherein said
electric connecting portion is a connector which can be removably connected
to an external electric element.

5. An optical wiring device according to claim 1, wherein said
optical transmission means includes a light input/output end surface, and
said optical device is surface-implemented on said light input/output end
surface with said surface light emitting or receiving device abutting said

light input/output end surface.

6. An optical wiring device according to claim 1, wherein said surface light emitting device is a surface emitting laser.

5

7. An optical wiring device according to claim 1, wherein said surface light receiving device is a p-i-n photodiode.

8. An optical wiring device according to claim 1, wherein said surface light receiving device is a metal-semiconductor-metal (MSM) photodiode.

9. An optical wiring device according to claim 1, further comprising a wiring substrate, and wherein a plurality of said optical devices with respective independent electrodes are arranged in an array, said respective independent electrodes are bonded to said wiring substrate in a flip-chip manner, and said electric connecting portion includes a plurality of conductors to which said independent electrodes are electrically connected through said wiring substrate, respectively.

20

10. An optical wiring device according to claim 9, wherein said wiring substrate includes a window for passing light from or to said optical device, and said optical device is optically coupled and bonded to said optical transmission means in a surface-implementation manner, with said wiring substrate interposed between said optical device and said optical transmission means.

11. An optical wiring device according to claim 9, further comprising a plate with a window for passing light from or to said optical device, and wherein said optical device is optically coupled and bonded to said optical transmission means in a surface-implementation manner with
5 said plate interposed between said optical device and said optical transmission means.

12. An optical wiring device according to claim 9, wherein said wiring substrate is a flexible wiring substrate which is freely flexible.
10

13. An optical wiring device according to claim 1, further comprising an electronic circuit device for driving said optical device, said electronic circuit device being integrated on said electric connecting portion.
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14. An optical wiring device according to claim 13, wherein said electronic circuit device has a parallel-to-serial converting function.

15. An optical wiring device according to claim 1, further comprising an electronic circuit device in which said optical device is electrically connected to an integrated circuit (IC), and wherein said optical device and said IC are integrated on a common Si substrate in a hybrid manner, and said optical device is implemented on said common Si substrate in a flip-chip manner.
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16. An optical wiring device according to claim 9, wherein said optical device is a surface emitting laser sandwiched between said optical

transmission means and said wiring substrate, and said surface emitting laser is comprised of a cavity with an active layer sandwiched between multi-layer reflective mirrors with a growth semiconductor substrate removed.

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17. An optical wiring device according to claim 9, further comprising a plate, and wherein said optical device is a surface emitting laser sandwiched between said optical transmission means and said plate, and said surface emitting laser is comprised of a cavity with an active layer sandwiched between multi-layer reflective mirrors with a growth semiconductor substrate removed.

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18. An optical wiring device according to claim 1, wherein said optical transmission means comprises a waveguide sheet in which waveguide cores are arranged in an array.

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19. An optical wiring device according to claim 1, wherein said optical transmission means comprises an array of optical fibers.

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20. An optical wiring device according to claim 1, wherein said optical transmission means includes a metal thin film for conducting an electric connection.

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21. An optical wiring device according to claim 1, wherein said electric connecting portion is constructed such that said electric connecting portion is removably connected to an external receptacle of an electronic apparatus.

22. An optical wiring device according to claim 1, wherein said electric connecting portion is constructed such that said electric connecting portion is electrically connected with a solder to a printed board provided in an electronic apparatus.

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23. An optical wiring device according to claim 1, wherein said optical device is a surface emitting laser, said surface emitting laser is driven directly by the on-and-off of a complementary metal-oxide semiconductor (CMOS) buffer at an output stage of an external apparatus that is connected to said electric connecting portion, and a driving current of said surface emitting laser is controlled by a resistor inserted serially with said surface emitting laser.

10

24. An optical wiring device according to claim 1, wherein said optical device is a surface emitting laser, said surface emitting laser is driven directly by the on-and-off of a complementary metal-oxide semiconductor (CMOS) buffer at an output stage of an electronic circuit which is provided between said surface emitting laser and said electric connecting portion and electrically connected to said electric connecting portion, and a driving current of said surface emitting laser is controlled by a resistor inserted serially with said surface emitting laser.

15

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25. An electronic apparatus comprising:

a printed circuit board with an electronic integrated device; and

25

an optical wiring device for connecting said printed circuit board to another apparatus, said optical wiring device including (a) an electric connecting portion, (b) an optical transmission means for transmitting an

optical signal, and (c) an optical device for conducting an optoelectric conversion, wherein said optical device is provided between said electric connecting portion and said optical transmission means, and said optical device is integrated and includes at least one of a surface light emitting device, which is modulated by an electric signal supplied through said electric connecting portion, and a surface light receiving device, which converts an optical signal transmitted through said optical transmission means to an electric signal.

26. An electronic apparatus comprising:
a computer;
a display with an input /output (I/O) port; and
an optical wiring device for connecting said computer to said display through said I/O port, said optical wiring device including (a) an electric connecting portion, (b) an optical transmission means for transmitting an optical signal, and (c) an optical device for conducting an optoelectric conversion, wherein said optical device is provided between said electric connecting portion and said optical transmission means, and said optical device is integrated and includes at least one of a surface light emitting device, which is modulated by an electric signal supplied through said electric connecting portion, and a surface light receiving device, which converts an optical signal transmitted through said optical transmission means to an electric signal.

27. An electronic apparatus comprising:
a computer;
a network; and

an optical wiring device for connecting said computer to said network, said optical wiring device including (a) an electric connecting portion, (b) an optical transmission means for transmitting an optical signal, and (c) an optical device for conducting an optoelectric conversion, wherein said optical device is provided between said electric connecting portion and said optical transmission means, and said optical device is integrated and includes at least one of a surface light emitting device, which is modulated by an electric signal supplied through said electric connecting portion, and a surface light receiving device, which converts an optical signal transmitted through said optical transmission means to an electric signal.

ABSTRACT OF THE DISCLOSURE

An optical wiring device includes an electric connecting portion, an optical transmission unit for transmitting an optical signal, and an optical device for conducting an optoelectric conversion. The optical device is provided between the electric connecting portion and the optical transmission unit. The optical device includes at least one of a surface light emitting device, which is modulated by an electric signal supplied through the electric connecting portion, and a surface light receiving device, which converts an optical signal transmitted through the optical transmission unit to an electric signal.

FIG.1
(PRIOR ART)

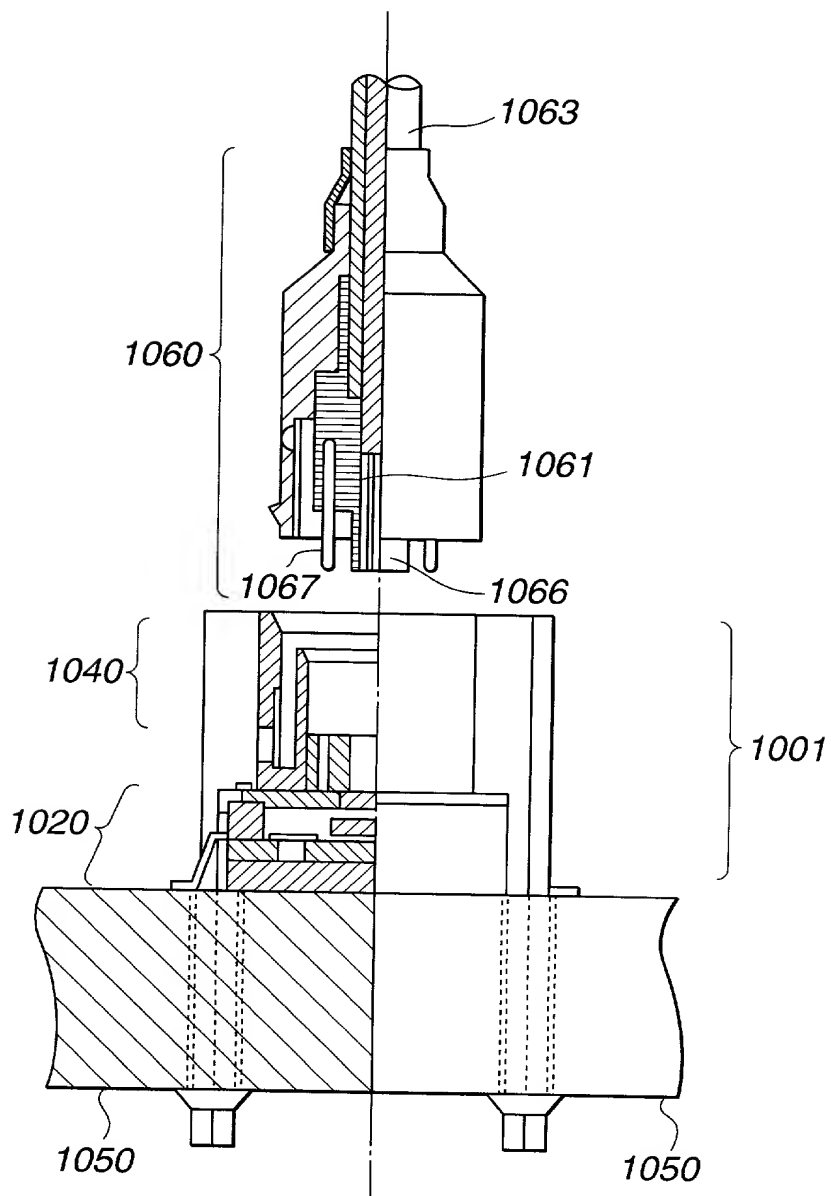


FIG.2
(PRIOR ART)

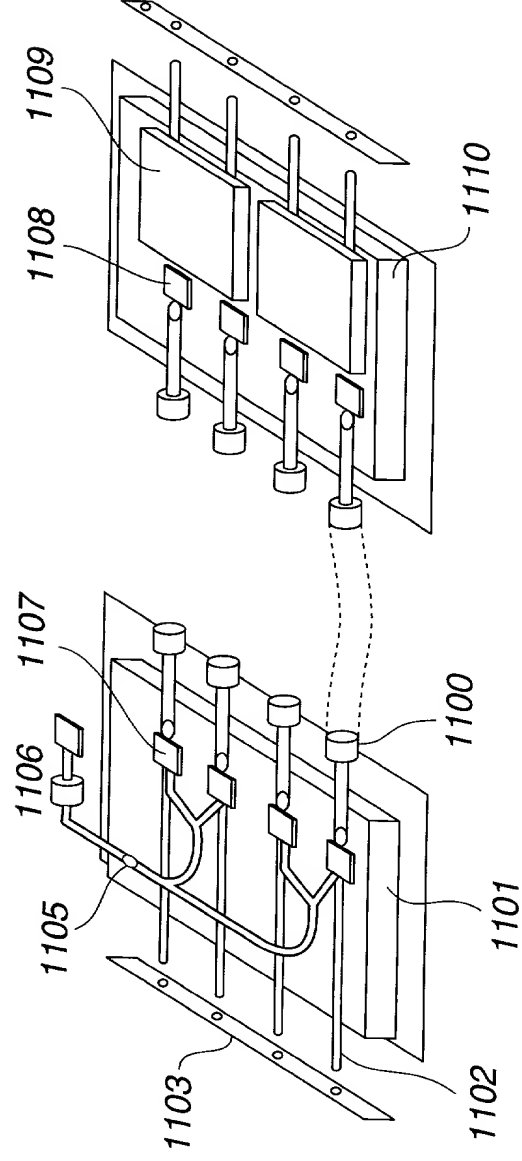


FIG.3A

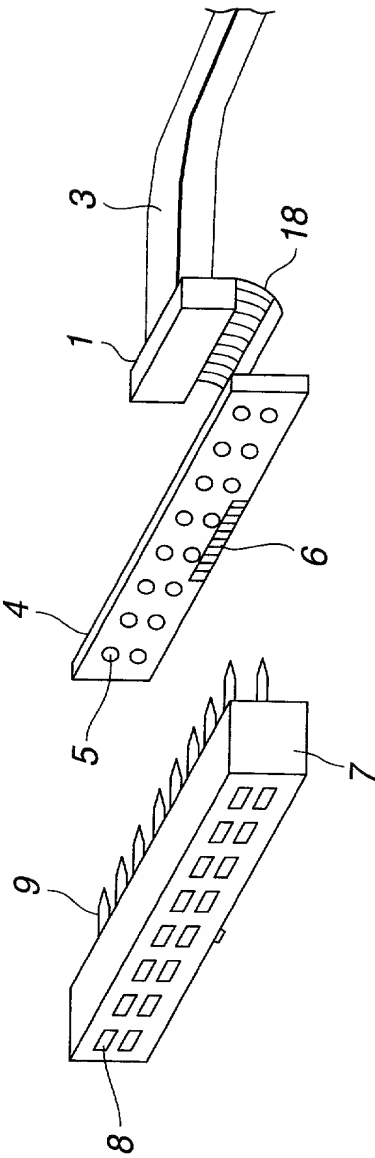


FIG.3B

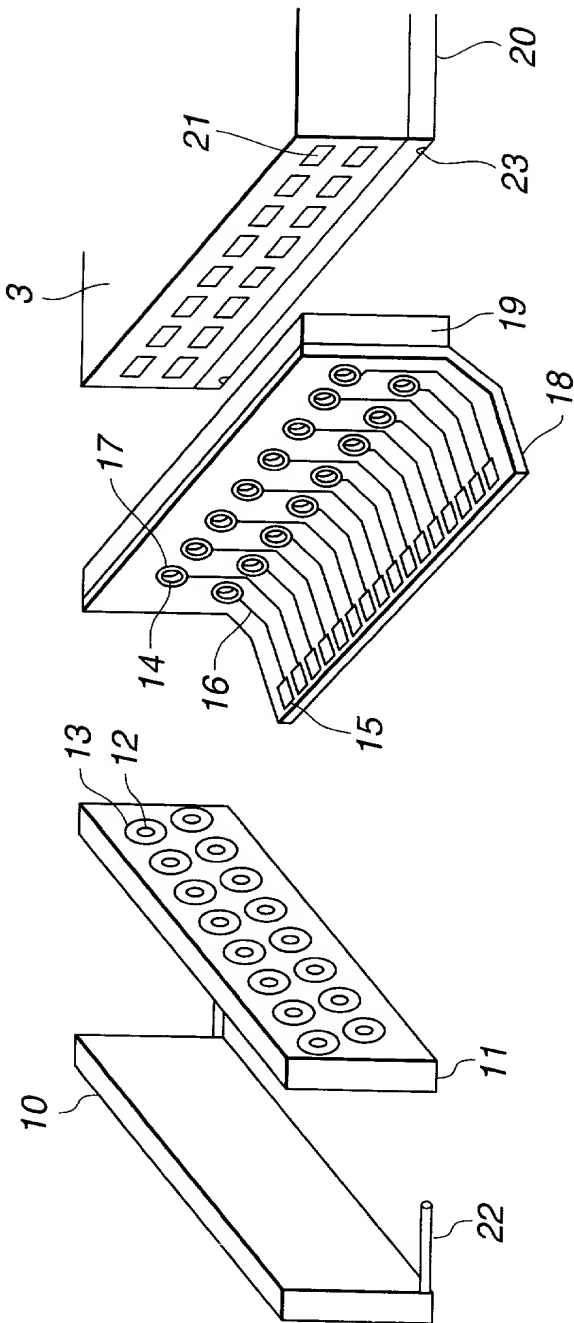


FIG.4

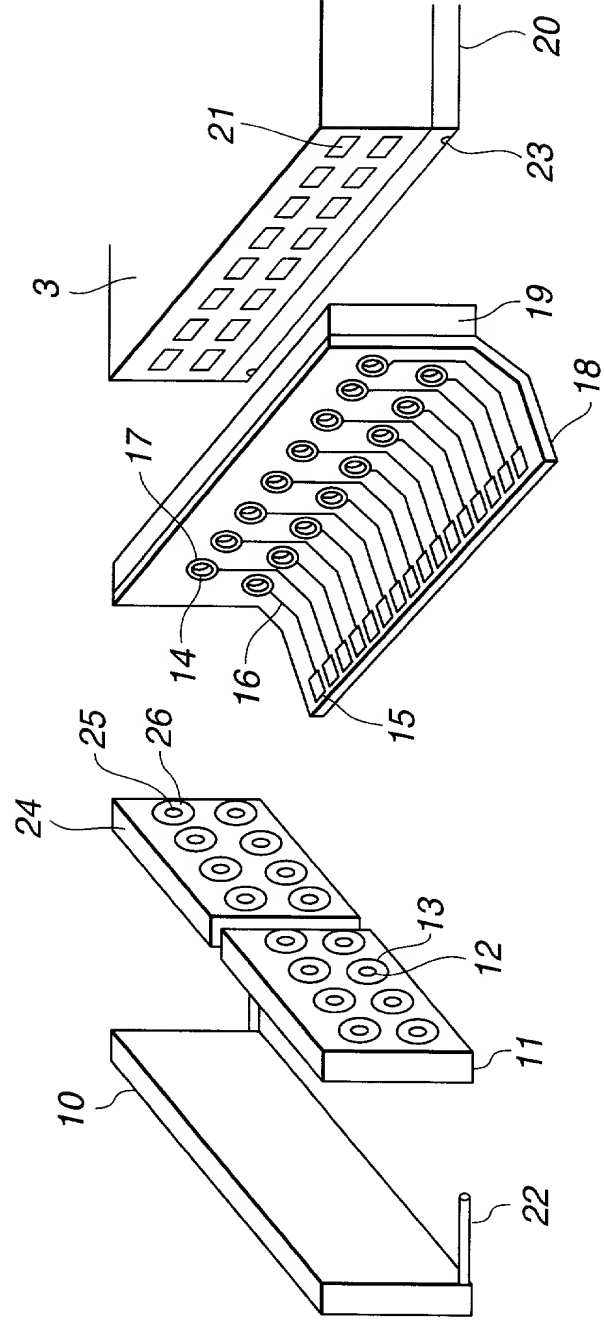


FIG.5

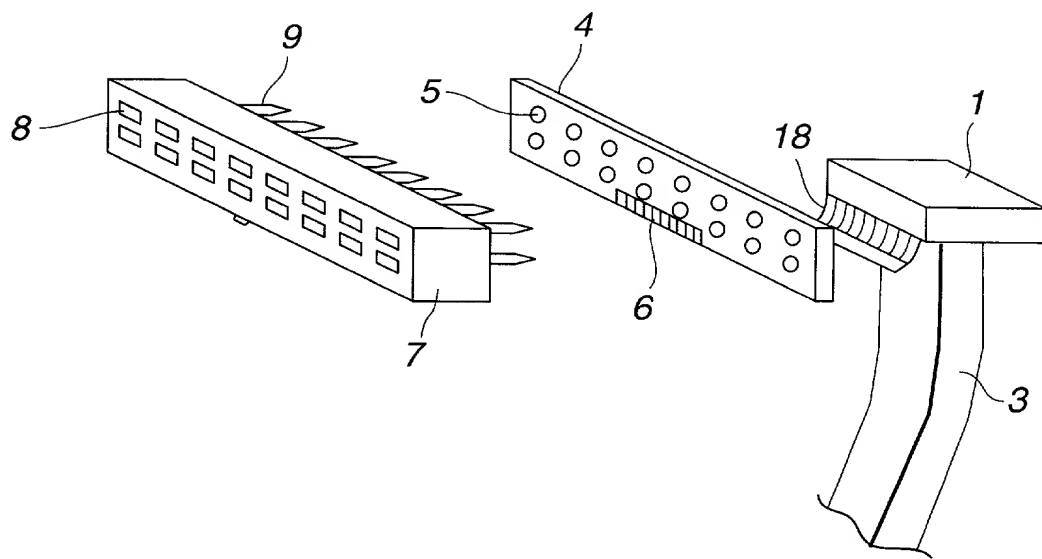


FIG.6A

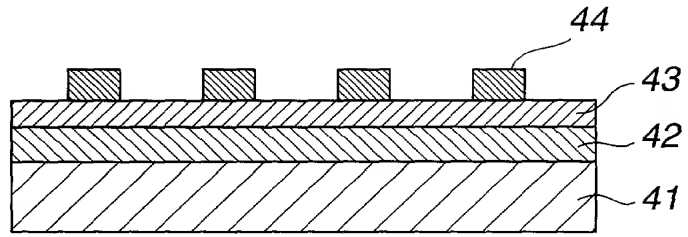


FIG.6B

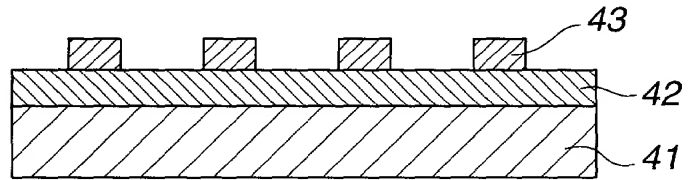


FIG.6C

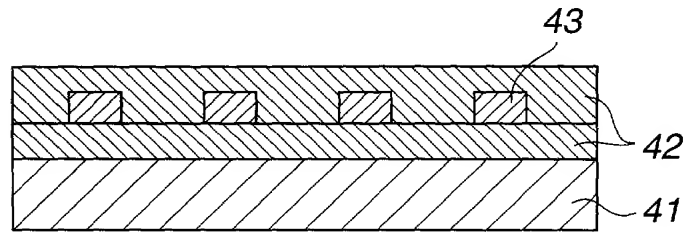


FIG.6D

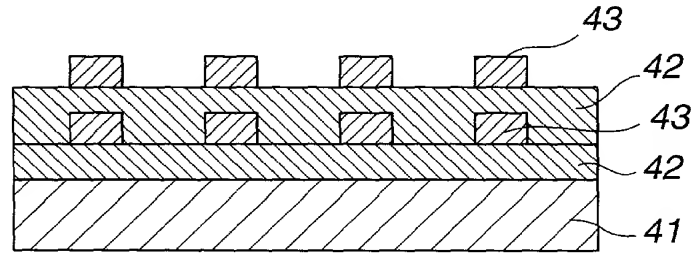


FIG.6E

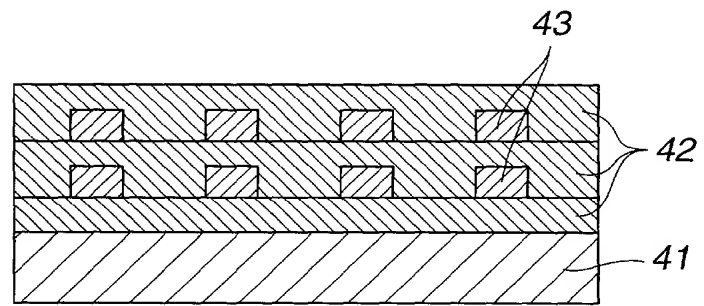


FIG.6F

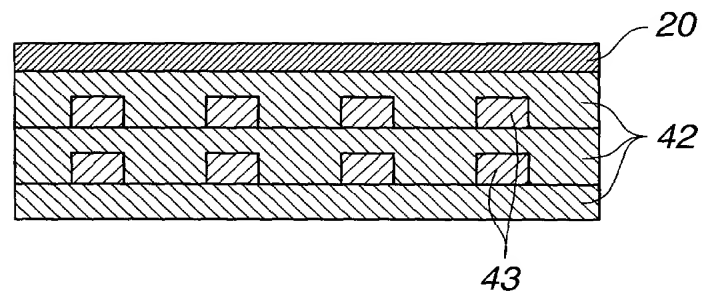


FIG.7

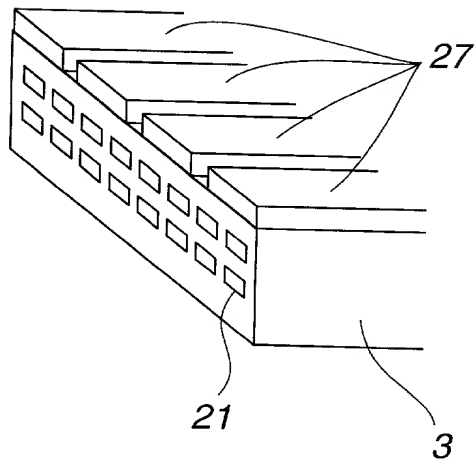


FIG.8A

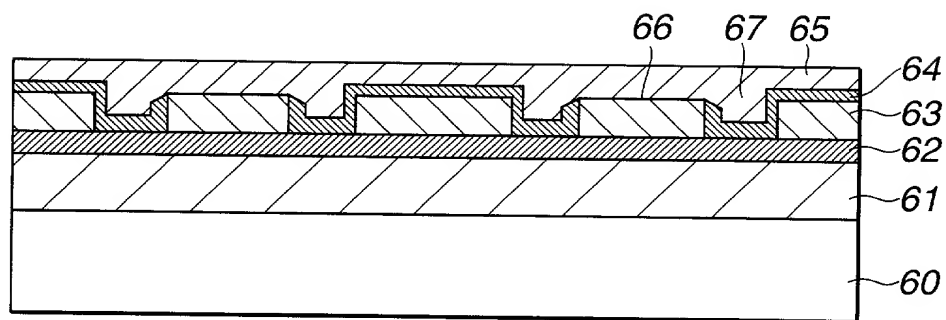


FIG.8B

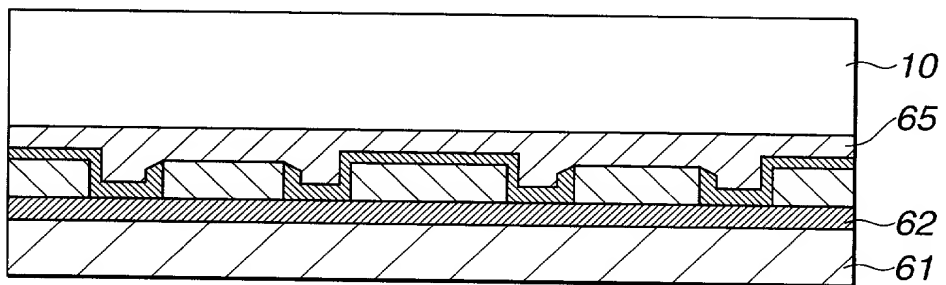


FIG.8C

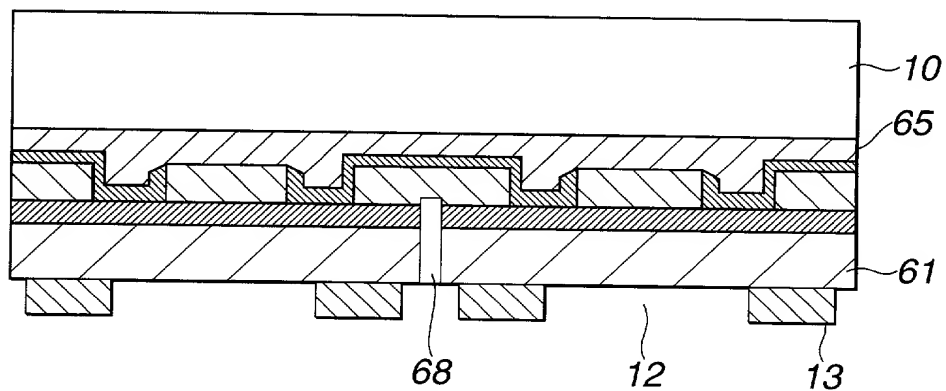


FIG.8D

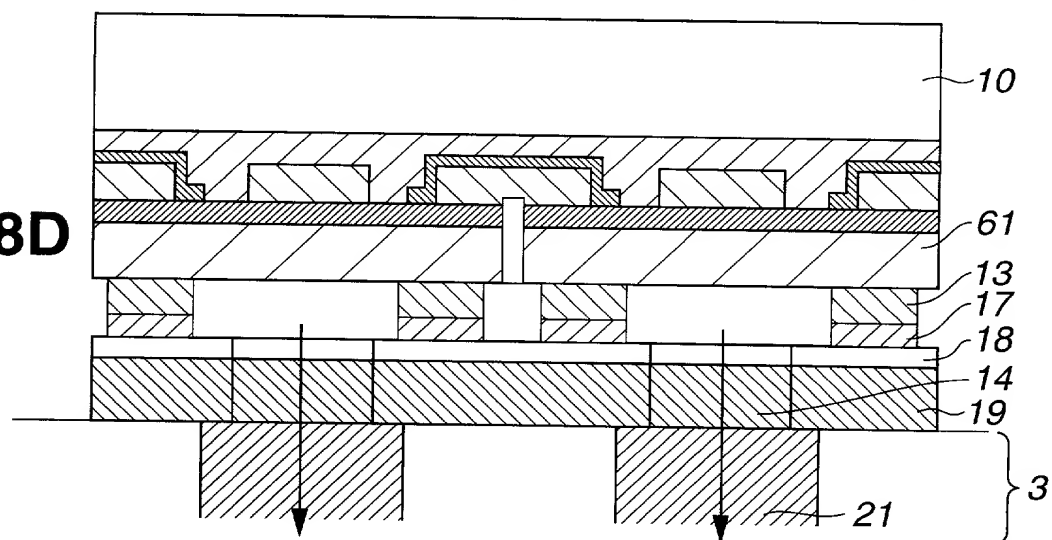


FIG.9A

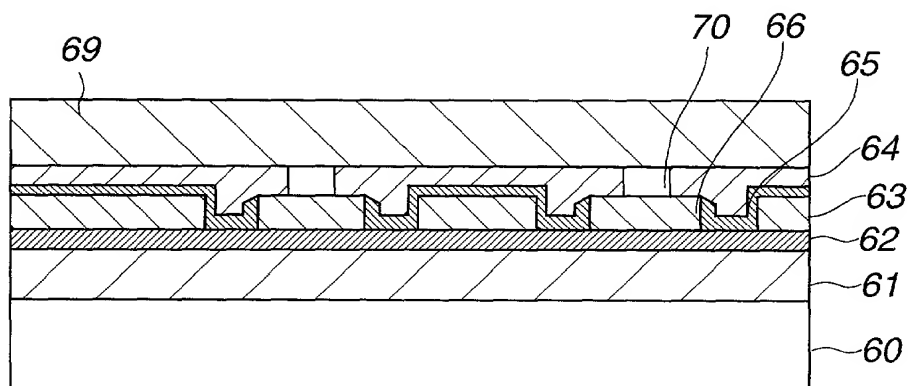


FIG.9B

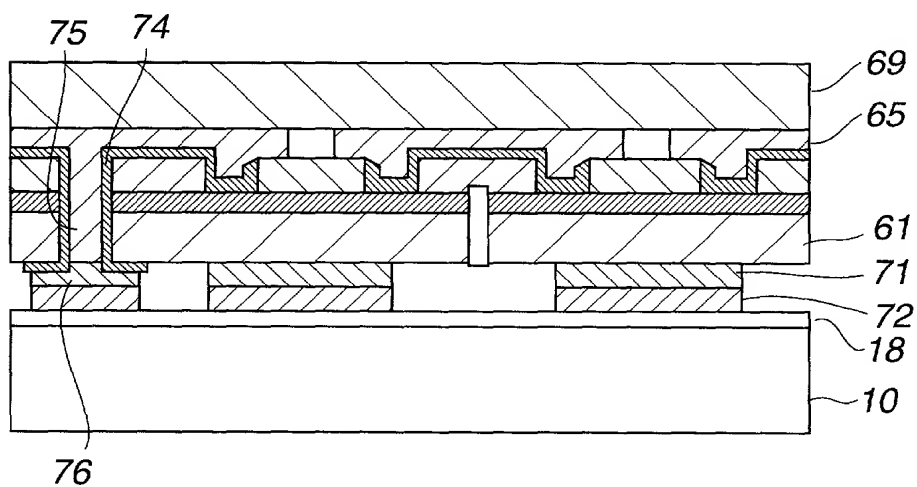


FIG.9C

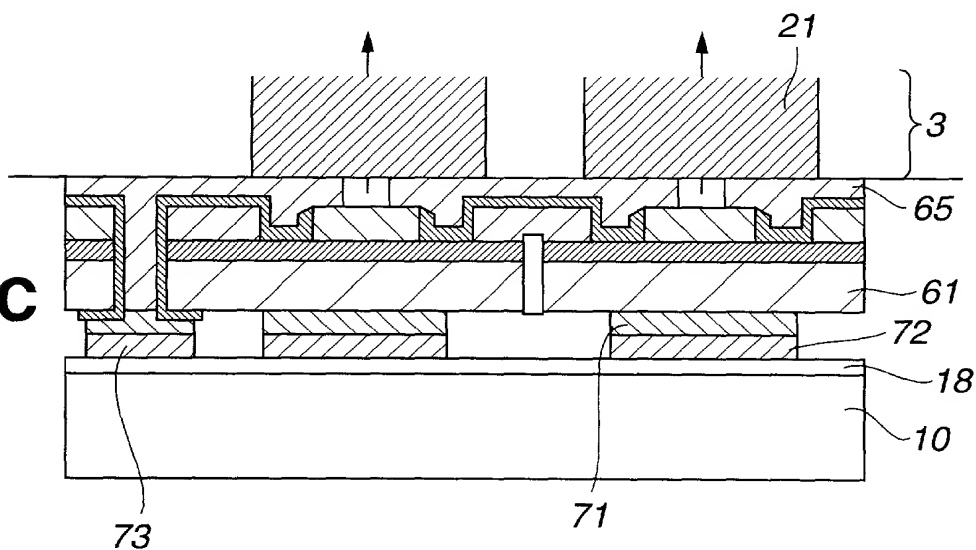


FIG.10A

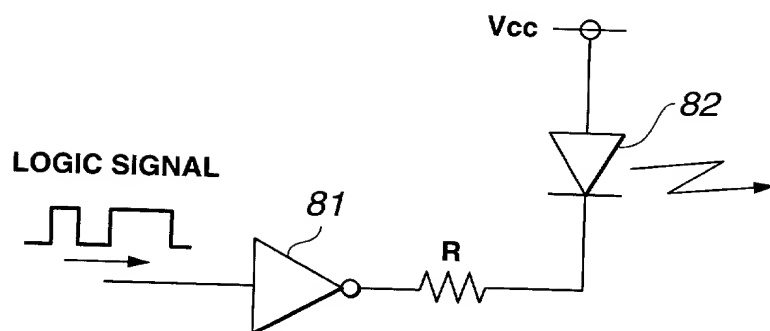


FIG.10B

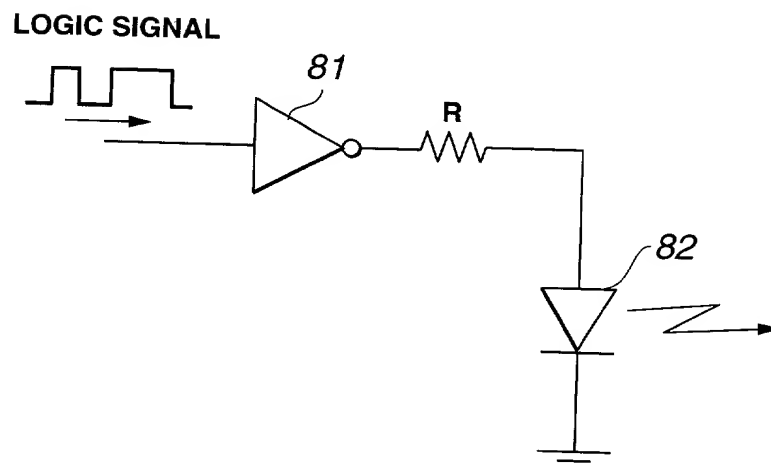


FIG.11A

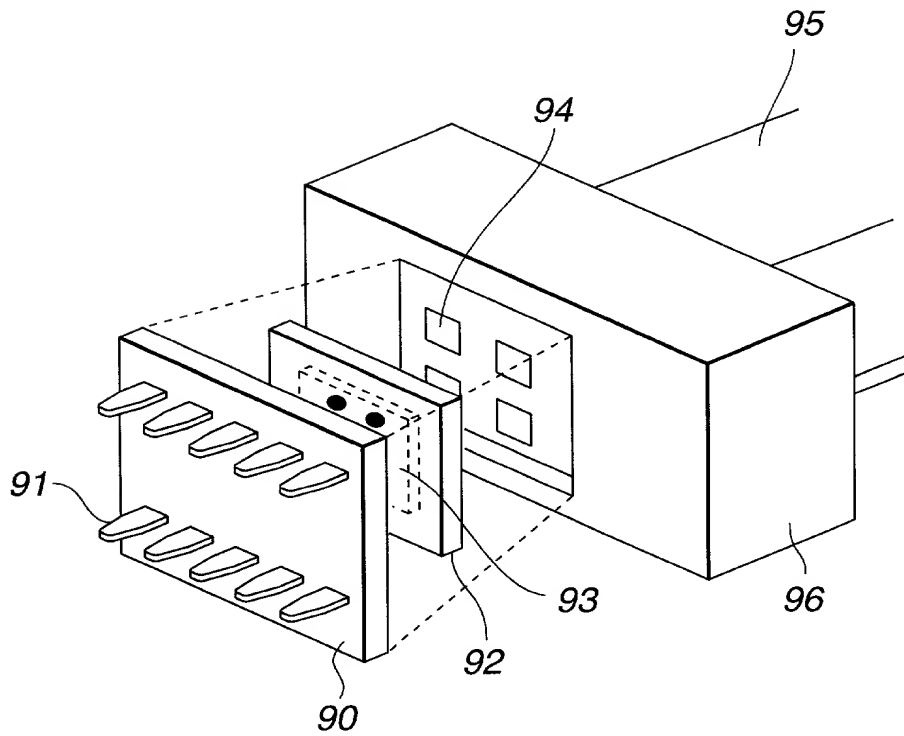


FIG.11B

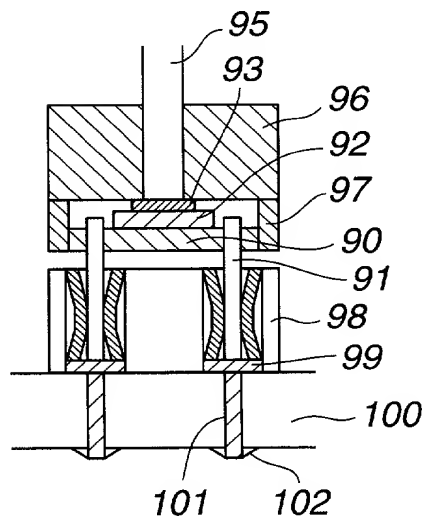


FIG.11C

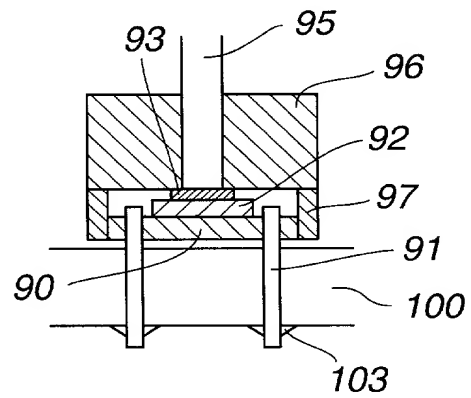


FIG.12

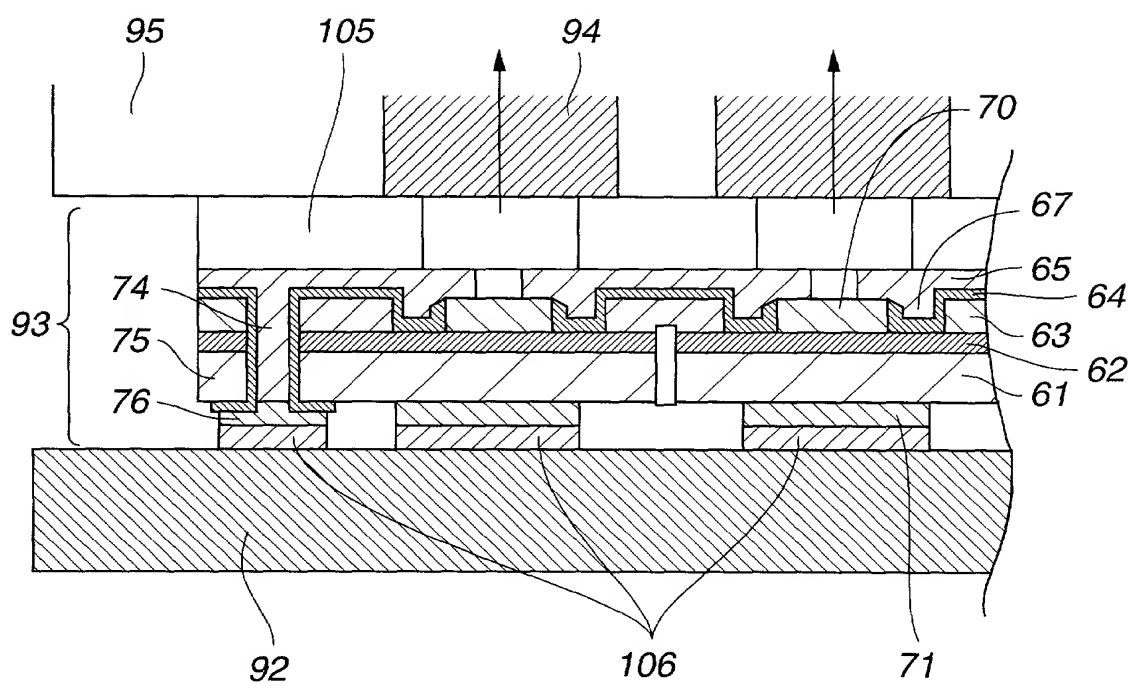


FIG.13

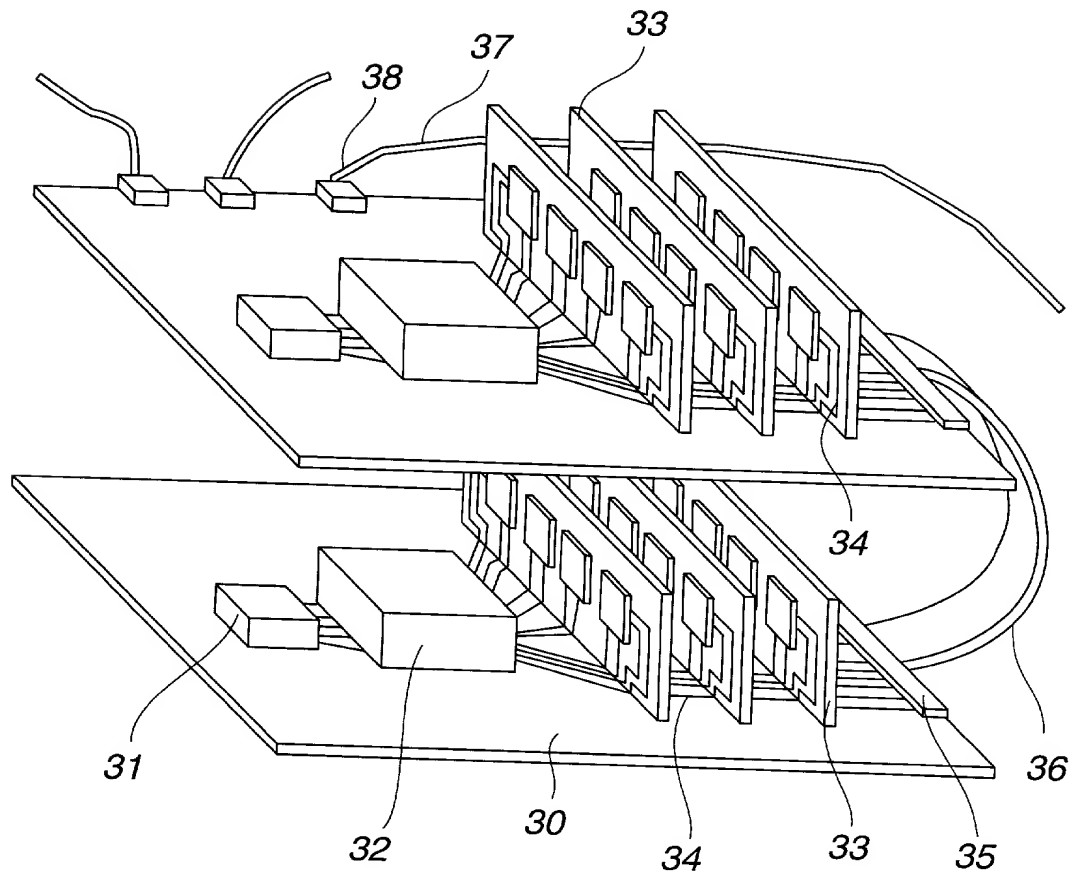
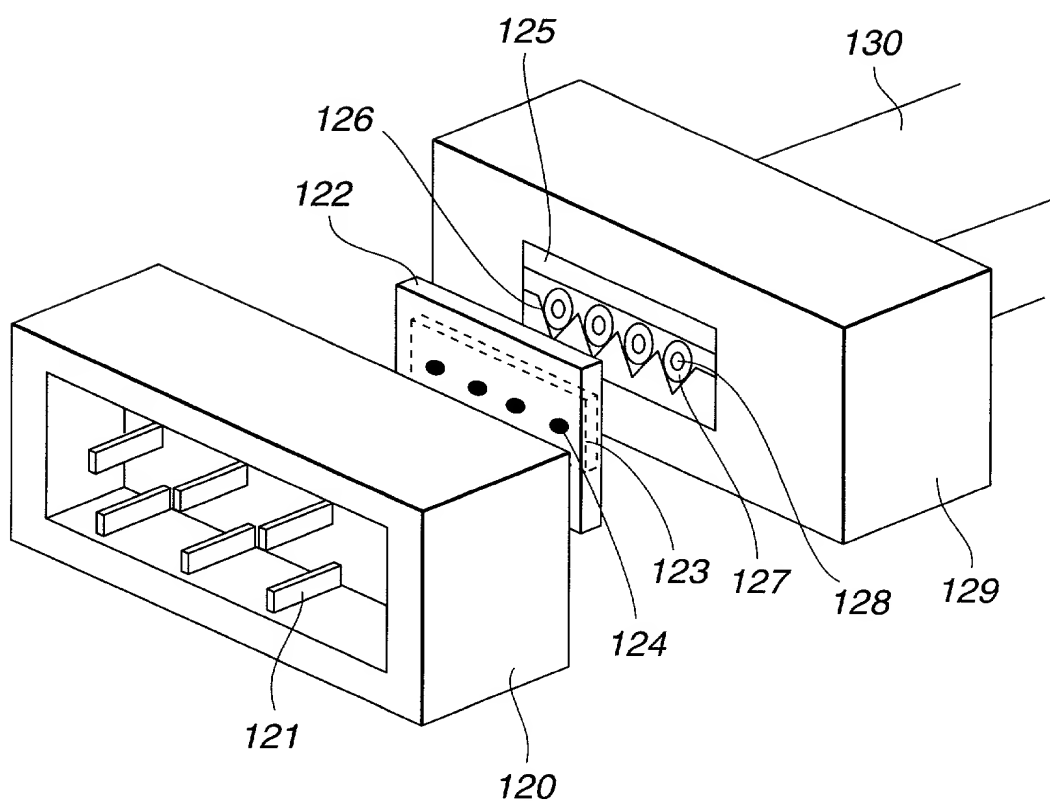
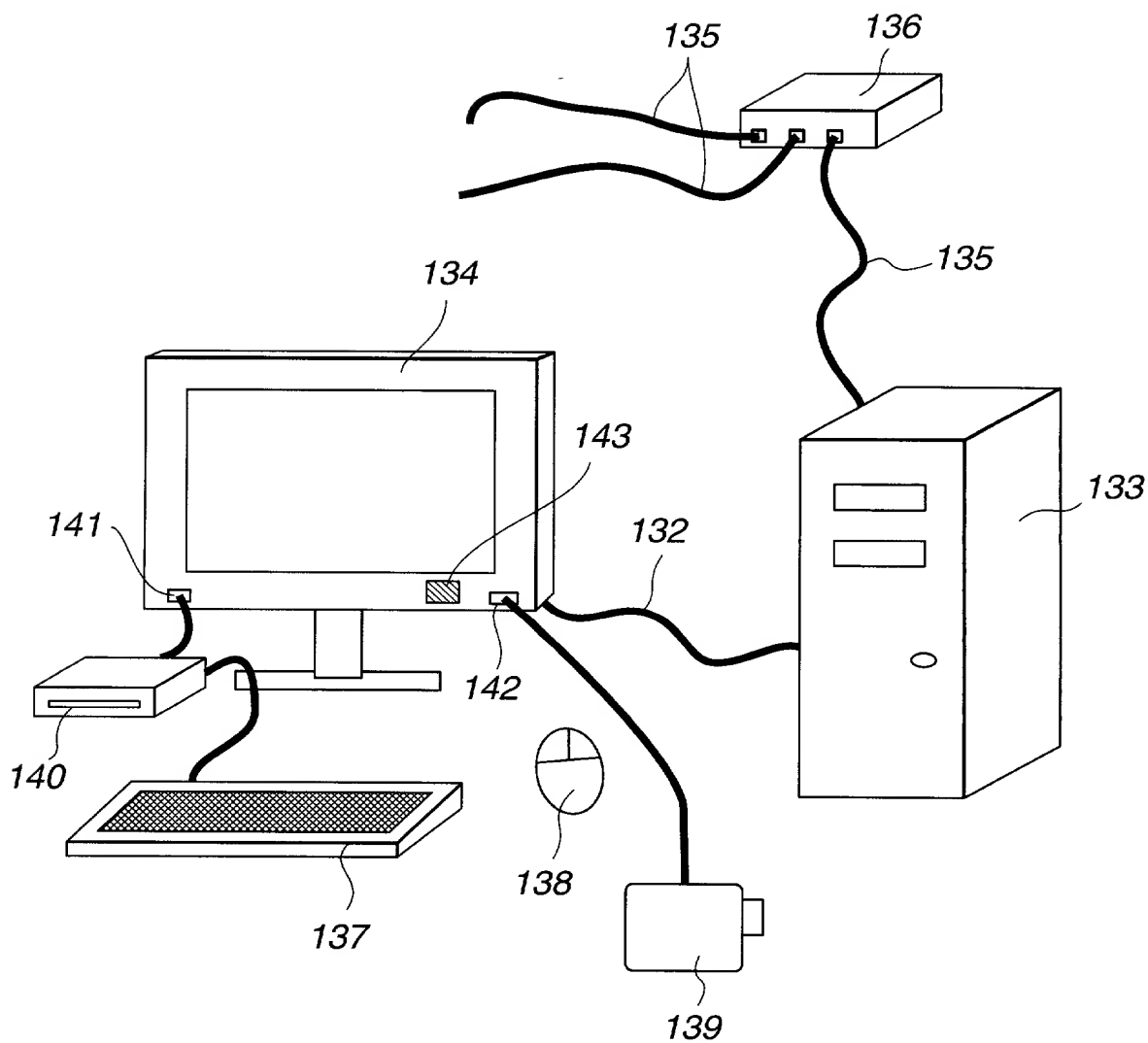


FIG.14



100 100



**COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION**
(page 1)

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled OPTICAL WIRING DEVICE
the specification of which ☒ is attached hereto. ☐ was filed on _____ as United States Application No. or PCT International Application No. _____
and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR §1.56.

I hereby claim foreign priority benefits under 35 U.S.C. §119(a)-(d) or §365(b), of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT international application which designates at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT international application having a filing date before that of the application on which priority is claimed

<u>Country</u>	<u>Application No.</u>	<u>Filed (Day / Mo. / Yr.)</u>	<u>(Yes / No) Priority Claimed</u>
Japan	213005/1999 (Pat.)	28/July/1999	Yes

I hereby claim the benefit under 35 U.S.C. §120 of any United States application(s), or §365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. §112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

<u>Application No.</u>	<u>Filed (Day/Mo./Yr.)</u>	<u>Status (Patented, Pending, Abandoned)</u>

I hereby appoint the practitioners associated with the firm and Customer Number provided below to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and direct that all correspondence be addressed to the address associated with that Customer Number:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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